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INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Application Number	10/767,540
			Filing Date	01-29-2004
			First Named Inventor	Dwarakanath
			Art Unit	2838
			Examiner Name	Behm, Harry Raymond
(Use as many sheets as necessary)			Attorney Docket Number	ENP-003
Sheet	1	of	1	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/HB/	1	BARRADO, A., <i>et al.</i> , "New DC/DC Converter with Low Output Voltage and Fast Transient Response," Proceedings of the IEEE Applied Power Electronics Conference, 2003, pp. 432-437, IEEE, Los Alamitos, CA.	
/HB/	2	GODER, D., <i>et al.</i> , "V ² Architecture Provides Ultra-Fast Transient Response in Switch Mode Power Supplies," Proceedings of HFPC Power Conversion, 1996, pp. 414-420.	
/HB/	3	"Linear Technology: LTC3736-1: Dual 2-Phase, No R _{SENSE} TM , Synchronous Controller with Spread Spectrum," 2004, 28 pp., Linear Technology Corporation, Milpitas, CA.	
/HB/	4	PATELLA, B.J., <i>et al.</i> , "High-Frequency Digital Controller IC for DC/DC Converters," IEEE Proceedings of the Applied Power Electronics Conference, March 10, 2002, 7 pp., IEEE, Los Alamitos, CA.	
/HB/	5	PETERCHEV, A.V., <i>et al.</i> , "Quantization Resolution and Limit Cycling in Digitally Controlled PWM Converters," IEEE Transactions on Power Electronics, January 2003, pp. 301-303, Vol. 18, No. 1, IEEE, Los Alamitos, CA.	
/HB/	6	REDL, R., <i>et al.</i> , "Optimizing the Load Transient Response of the Buck Converter," Proceedings of the IEEE Applied Power Electronics Conference, 1998, pp. 170-176, IEEE, Los Alamitos, CA.	
/HB/	7	SCHONEMAN, G.K., <i>et al.</i> , "Output Impedance Considerations for Switching Regulators with Current-Injected Control," Proceedings of the 18th Annual IEEE Power Electronics Specialists Conference, June 1987, pp. 324-335, IEEE, Los Alamitos, CA.	
/HB/	8	SOTO, A., <i>et al.</i> , "Analysis of the Buck Converter for Scaling the Supply Voltage of Digital Circuits," Proceedings of the IEEE Applied Power Electronics Conference, 2003, pp. 711-717, IEEE, Los Alamitos, CA.	
/HB/	9	SOTO, A., <i>et al.</i> , "Design Methodology for Dynamic Voltage Scaling in the Buck Converter," Proceedings of the IEEE Applied Power Electronics Conference, 2005, pp. 263-269, IEEE, Los Alamitos, CA.	
/HB/	10	"TPS40100: Midrange Input Synchronous Buck Controller with Advanced Sequencing and Output Margining," May 2005, 37 pp., Texas Instruments Incorporated, Dallas, TX.	

Examiner Signature	/Harry Behm/	Date Considered	09/11/2007
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

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